

Case No. 00ED18852609




DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE
WASHINGTON, DC 20231

In re Application of: **RAYNOR ET AL.**
Serial No.: **Not yet assigned**
Filed: **Herewith**
For: **SOLID STATE IMAGING DEVICE**
Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.

1. ☒ This IDS is submitted under 37 C.F.R. § 1.97. No fee is required.
2. ☐ This IDS is submitted under 37 C.F.R. § 1.97(c). Enclosed is a check in the amount of \$ 180.00.
3. ☐ This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required.
4. ☐ This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check in the amount of \$ 130.00 to cover the petition fee.
5. ☒ **The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.**

Date: November 16, 2001



MICHAEL W. TAYLOR
Reg. No. 43,182

INFORMATION DISCLOSURE STATEMENT	Atty Docket:	00ED18852609
	Serial No.:	Not Yet Assigned
	Applicant:	RAYNOR ET AL.
	Filing Date:	Herewith
	Group:	

1-978 U.S. PTO
09/993387

**U.S. PATENT DOCUMENTS**

Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
	AA	5,055,930	10/8/91	Nagasaki et al.	358	213.11	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub Class	Translation
	AM						
	AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AQ	Itoh et al. "4-Layer 3-D IC with a Function of Parallel Signal Processing" Microelectronic Engineering, NL, Elsevier Publishers BV., Amsterdam, vol. 15, no. 1/04, 10/1/1991, pages 187-190, XP000292757					
	AR	Akasaka et al. "The 3-D IC with 4-Layer Structure for the Fast Range Sensing System" Microelectronic Engineering, NL, Elsevier Publishers BV., Amsterdam, vol. 15, no. 1/04, 10/1/1991, pages 183-186, XP000292756					
	AS	Zhou et al. "Frame-Transfer CMOS Active Pixel Sensor with Pixel Binning" IEEE Transactions on Electron Devices, US, IEEE Inc. New York, vol. 44, no. 10, 10/1/1997, pages 1764-1768, XP000703891					

EXAMINER:	DATE CONSIDERED:
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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